

PROFILE OF HOD(EEE)



A.SUSHEELA, M.Tech.,
Email:susheela1812@gmail.com
Mobile: 9032110903

OBJECTIVE

To join a challenging position in a professionally managed organization and to grow along with the organization by contributing efforts towards its growth.

EDUCATION

Course	Discipline/ specialization	School/college	Board/ university	Year of passing	% OF MARKS	CLASS
M.TECH	EPS	JBIT, HYD	J N T U, HYD	2008	75	DISTINCTION
B.TECH	EEE	ARKAY, BODHAN	J N T U, HYD	2005	61	FIRST
INTER	M.P.C	VIJAY SAI Jr.college, BODHAN	BIE	2001	65	FIRST
S.S.C	S.S.C	VIJAY SAI SCHOOL, BODHAN	S.S.C	1999	66	FIRST

EXPERIENCE

*I worked as an **Asst. Professor** in EEE department in KCEA, Armoor, Nizamabad from **august 2005 to September 2007.**

*Presently working as **Head of the** department in VREC Nizamabad from **October 2007 to till date.**

TECHNICAL SKILLS

- **Languages** :C
- **Electrical circuits design Package** :P-Spice, MATLAB
- **Hardware Skills** : Microprocessor (8086)

STRENGTHS AND CAPABILITIES

- Efficient, hardworking & Goal oriented.
- Commitment to Cooperative team work
- Take pride in doing good work & achieving good results
- Ability to face and negotiate changes in professional life
- Sincerity
- I guided B.Tech & M.Tech projects

WORKSHOP

- I attended two day national workshop on **SCADA AND APPLICATIONS**

PROJECTS

Title: LOW-POWER MIXED SIGNAL CVNS - BASED 64-BIT ADDER FOR MEDIA SIGNAL PROCESSING

Description: The aim of this project is designing of a low power mixed signal CVNS based 64-bit adder. CVNS stands for continuous valued number system. Mathematical solutions of CVNS for the low radix give elegant mixed signal solution. This results into reduces area and cost of hardware.

Title: Placement of Capacitor in Two Pole Turbo generator

Description: This Project describes the operation of generator by placing capacitor in order to control Load